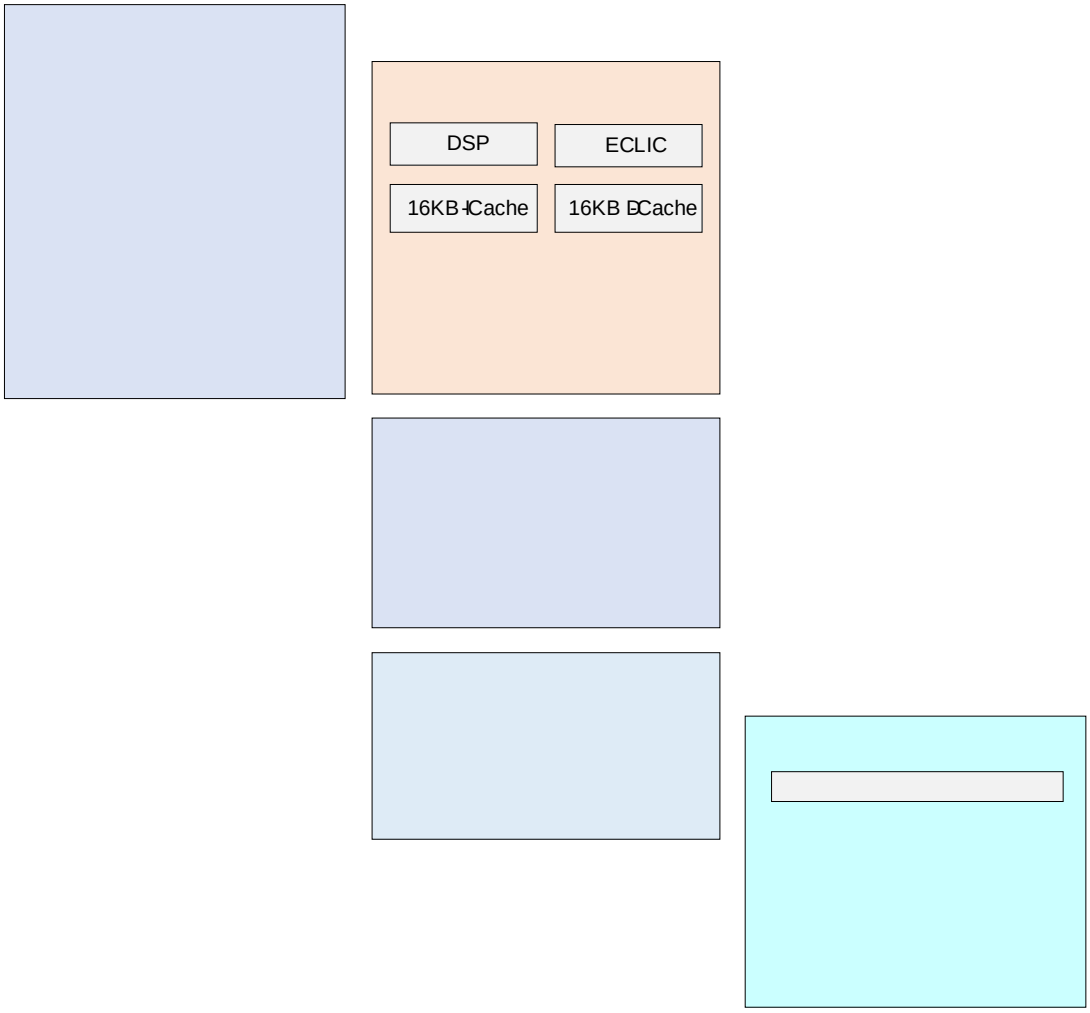
1

HX77xx SoC RISC-V
DP/eDP MIPI 4
LVDS 2 JPEG Audio
Codec MIPI USB3.1 USB2.0 SDIO
SPI I2C UART PMU

HX77xx

1 AR VR

2



2

[illegible]

□ [illegible] RSIC-V 32 [illegible] CPU [illegible] FPU DSP [illegible] 300Mhz
□ [illegible] 1MB System RAM
□ [illegible] 8KB Boot ROM
□ [illegible] 8 [illegible] DMA

[illegible]

□ [illegible] DP/eDP [illegible] RX [illegible] 5.6G*4lane